

**Amendments to the Specification:**

Please replace the paragraph that begins at page 10, line 6 through page 10, line 11 with the following amended version of that paragraph:

The logical  $V_{UP}$  voltage on control node 297 may be provided in a variety of ways. For example, the logical  $V_{UP}$  voltage may be the output of a comparator circuit (e.g., comparator circuit 250, as shown in FIG. 2, but not shown in FIGS. 3 and 4 for the sake of clarity) that compares the input voltage (e.g., the voltage on input node 291) with the minimum voltage required by a particular load.